

REMARKS/ARGUMENTS

Favorable reconsideration of this application is respectfully requested.

The specification is amended by the present response to correct minor informalities. The changes made to the specification are deemed to be self-evident from the original disclosure, and thus are not deemed to raise any issues of new matter.

Claims 1-12 are pending in this application. Claims 1, 2, 7, and 8 were rejected under 35 U.S.C. § 103(a) as unpatentable over Applicant's Admitted Art and U.S. patent application publication 2003/0194129 to Metz et al. (herein "Metz"). Claims 3-6 and 9-12 were rejected under 35 U.S.C. § 103(a) as unpatentable over Applicant's Admitted Art and Metz and U.S. patent 6,370,631 to Dye (herein "Dye '631").

Addressing the above-noted rejections, each of the above-noted rejections is traversed by the present response.

Applicant initially notes that independent claim 1 is amended by the present response to clarify features recited therein. Specifically, independent claim 1 now clarifies that elements of an image data processing unit are a graphics port, a peripheral device interconnection port, a print engine, and a first memory. Claim 1 now also clarifies that the first memory is "provided on a side of the graphics port with respect to the image data processing unit, the first memory for storing the image data". Independent claim 7 is also amended by the present response to more clearly recite an "image data processing unit" rather than the previously recited "interface unit". The structure clarified in claims 1 and 7 is believed to be fully supported for example by Figure 2 in the present specification.

The above-noted structure is believed to distinguish over the applied art.

First with respect to the admitted art of Figure 1, that admitted art does not disclose or suggest a memory, corresponding to the claimed "first memory", connected to a graphics port

of the image data processing unit (ASIC 1602) for storing image data to be transferred to a print engine.

Moreover, no teachings in Metz are believed to overcome the above-noted deficiencies in the admitted art.

The outstanding Office Action cites Figure 1 of Metz with respect to the claimed features. However, applicant respectfully submits that Figure 1 of Metz does not teach or suggest the use of a first memory provided on the side of a graphics port with respect to an image processing unit, the first memory storing image data processed by the image data processing unit. In such ways, no teachings in Metz can overcome the above-noted deficiencies in the admitted art.

Moreover, Metz does not even disclose a print engine connected to a peripheral device interconnection port, and thus Metz is directed to a different type of device as that in the claimed invention. The image engine 30 of Metz is part of a memory hub 16 or a north bridge. The image engine 30 shown in Figure 1 of Metz is connected to the local bus 14. Thus, Metz further does not disclose a print engine connected to a peripheral device interconnection port. Additionally, and as noted above, Metz does not disclose an image data processing unit having a graphic port and a peripheral device interconnection port. It appears that the CPU 12 shown in Figure 1 of Metz has only a graphics port, which structure differs from that recited in the claims.

Accordingly, applicant respectfully submits that no combination of teachings of the admitted art in view of Metz meets the limitations of amended independent claims 1 and 7, and the claims dependent therefrom.

Moreover, no teachings in Dye can overcome the above-noted deficiencies of the combination of teachings of the admitted art in view of Metz.

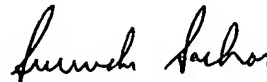
In such ways, applicant respectfully submits that each of the currently pending claims is allowable over the applied art.

As no other issues are pending in this application, it is respectfully submitted that the present application is in condition for allowance, and it is hereby respectfully requested that this case be passed to issue.

Respectfully submitted,

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